

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

4 Application of:

Kuei-Wu Huang, et al.

Examiner:

R. Booth

Serial No.

09/517,987

Art Unit:

2812

Filed:

March 3, 2000

Docket No.

94-C-096C3

METHOD OF FORMING PLANARIZED STRUCTURES IN AN INTEGRATED CIRCUIT

_2/28/01

CERTIFICATE OF MAILING

37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope with sufficient postage addressed to: Assistant Commissioner of Patents, Washington, D. C. 20231, on 301.

Signature

Assistant Commissioner for Patents Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed January 26, 2001, Applicant elects Group II; Claims 77-84, with traverse for further prosecution. A first Office Action on the merits is now respectfully awaited.

Please charge any fees necessary for prosecution of the present application to deposit account No. 19-1353.

Respectfully submitted,

Mario J. Donato

Attorney for Applicant

Reg. No. 37,816

STMicroelectronics, Inc. 1310 Electronics Drive, Mail Station 2346 Carrollton, TX 75006 972-466-7503